



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Bhattacharya et al.

Serial No.: 09/896,071

For: PROCESS FOR AUTOMATED GENERATION OF DESIGN-SPECIFIC COMPLEX FUNCTIONAL BLOCKS TO IMPROVE QUALITY OF SYNTHESIZED DIGITAL INTEGRATED CIRCUITS IN CMOS

Filed: June 29, 2001

Examiner: Thuan V. Do

Art Unit: 2825

Confirmation No.: 9529

Customer No.: 27,623

Attorney Docket No.: 162.7106USU

REQUEST FOR EXTENSION OF TIME

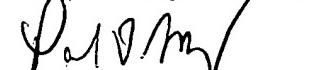
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

This is a petition for extension of time for a total period of one (1) month.
Based on the extension requested, the extended period for response will expire on
October 7, 2005.

A check in the amount of \$55.00 (small entity) is enclosed with this Petition.
The Commissioner is hereby authorized to charge any fees or credit any
overpayment, to Deposit Account No. 01-0467. A duplicate copy of this Request is
enclosed.

Respectfully submitted,



Paul D. Greeley, Esq.

Reg. No. 31,019

Attorney for Applicant(s)

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Date: October 6, 2005

10/12/2005 MBERHE 00000031 010467 09896071

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